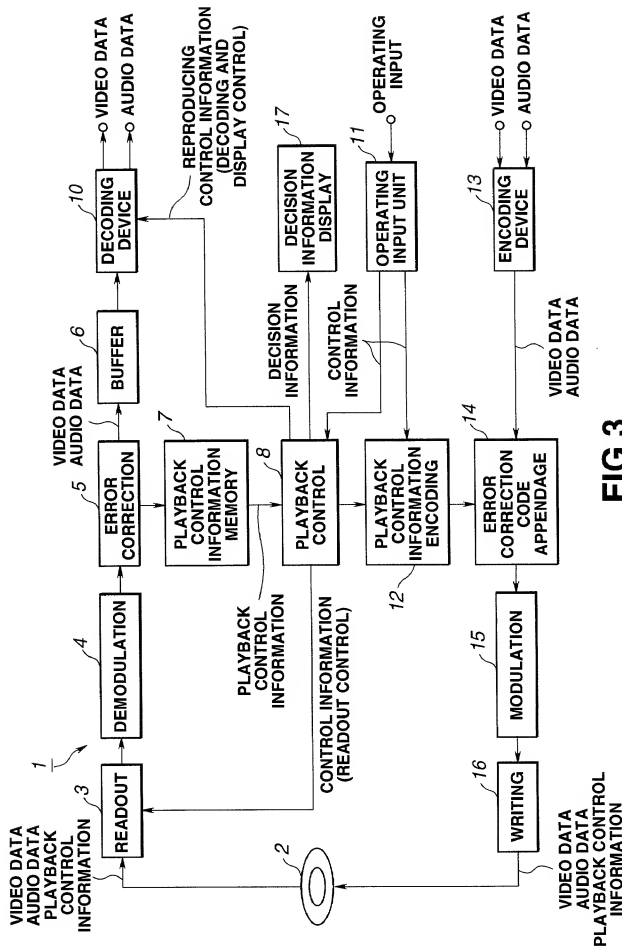


FIG.1A

FIG.1B





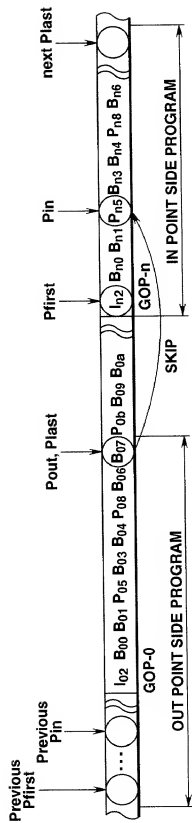
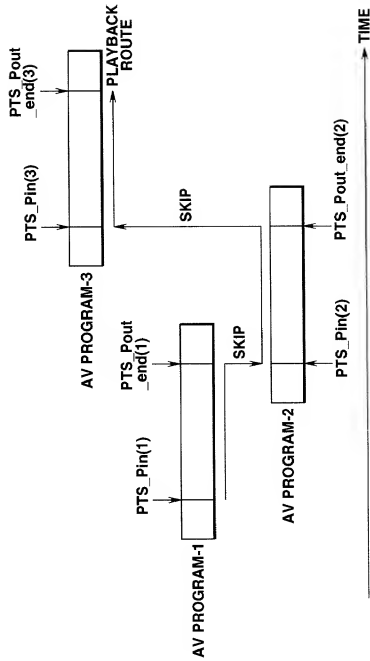


FIG.4



$$PTS\_Pin(2) = PTS\_Pout\_end(1) - STC\_offset(1)$$

$$STC\_offset(1) = PTS\_Pout\_end(1) - PTS\_Pin(2)$$

$$PTS\_Pin(3) = PTS\_Pout\_end(2) - STC\_offset(2)$$

$$STC\_offset(2) = PTS\_Pout\_end(2) - PTS\_Pin(3)$$

**FIG.5**

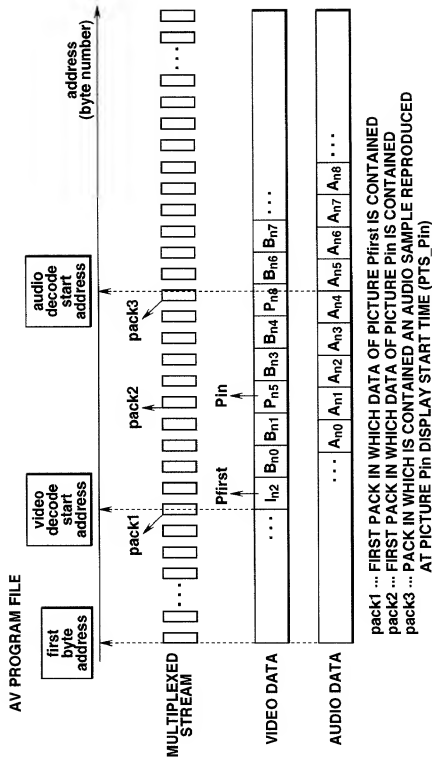


FIG.6

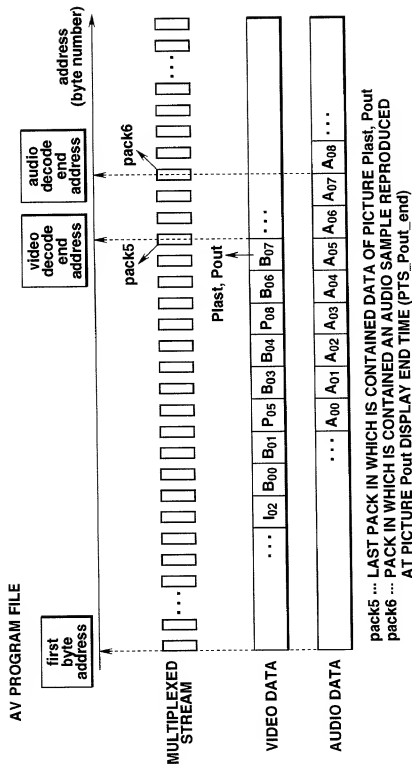
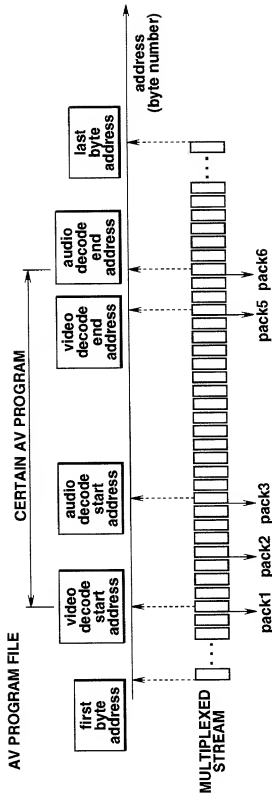


FIG.7



pack1 ... FIRST PACK IN WHICH DATA OF PICTURE Pfirst IS CONTAINED  
 pack2 ... FIRST PACK IN WHICH DATA OF PICTURE Pfin IS CONTAINED  
 pack3 ... PACK IN WHICH IS CONTAINED AN AUDIO SAMPLE REPRODUCED  
 AT PICTURE Pin DISPLAY START TIME (PTS\_Pin)  
 pack5 ... LAST PACK IN WHICH IS CONTAINED DATA OF PICTURE Plast  
 pack6 ... PACK IN WHICH IS CONTAINED AN AUDIO SAMPLE REPRODUCED  
 AT PICTURE Pout DISPLAY END TIME (PTS\_Pout\_end)

**FIG.8**



```

AV_program_File( ){
    do (
        pack( )
    while(nextbits( )==pack_start_code)
}

```

```

pack( ) {
    pack_header( )
    PES_packet( )
}

```

```

PES_packet( ){
    packet_header( )
    PTS
    DTS
    packet_payload( )
}

```

FIG.9

AV program file  
(program stream file)

```

playback_information_file( ){
    PTS_Pin
    PTS_Pout_end
    video_decode_start_address
    audio_decode_start_address
    video_decode_end_address
    audio_decode_end_address

    Pin_index
    Pin_type
    Plast_index

    Pin_first_field_parity
    Pout_last_field_parity
}

```

FIG.10

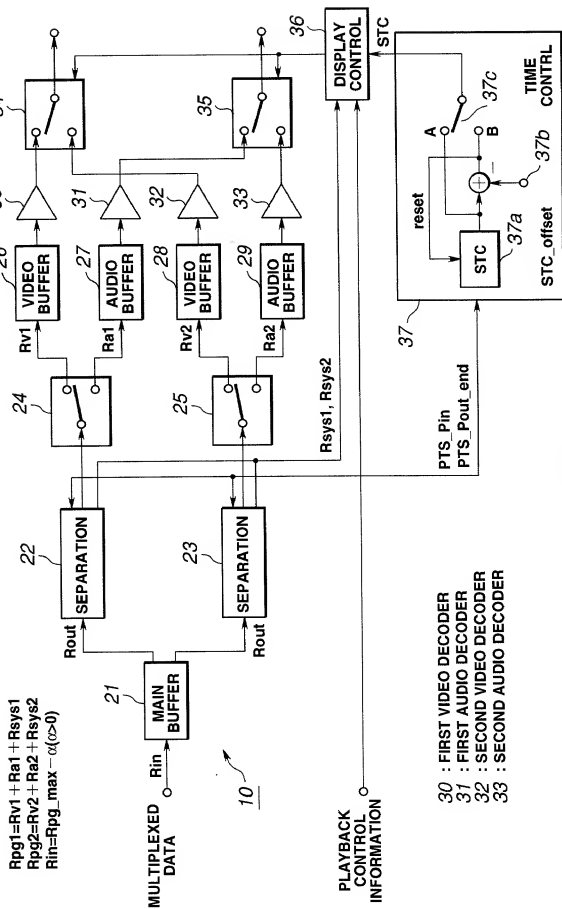


FIG.11

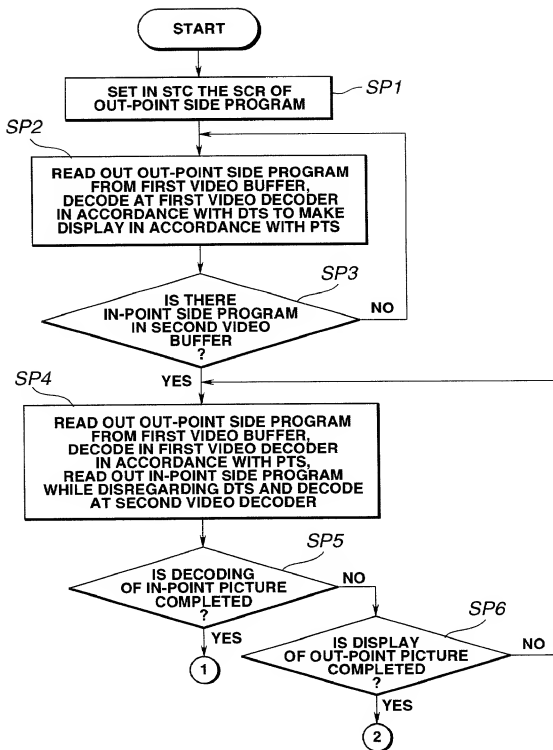


FIG.12

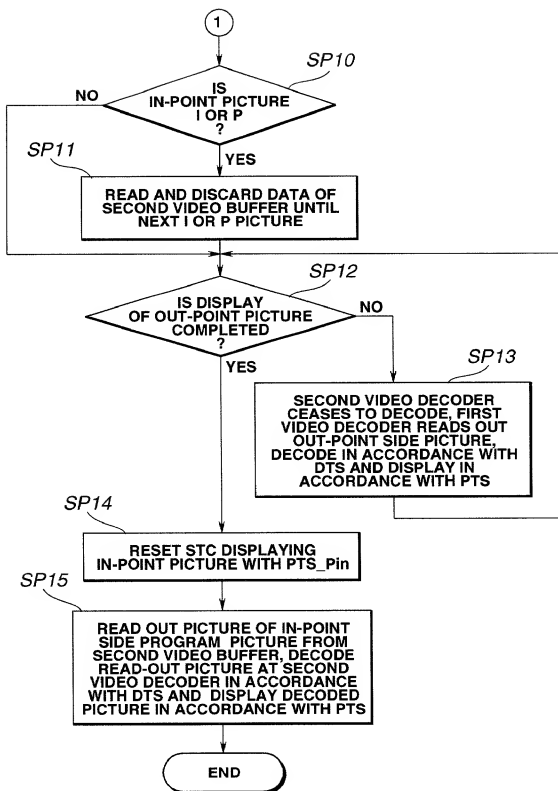
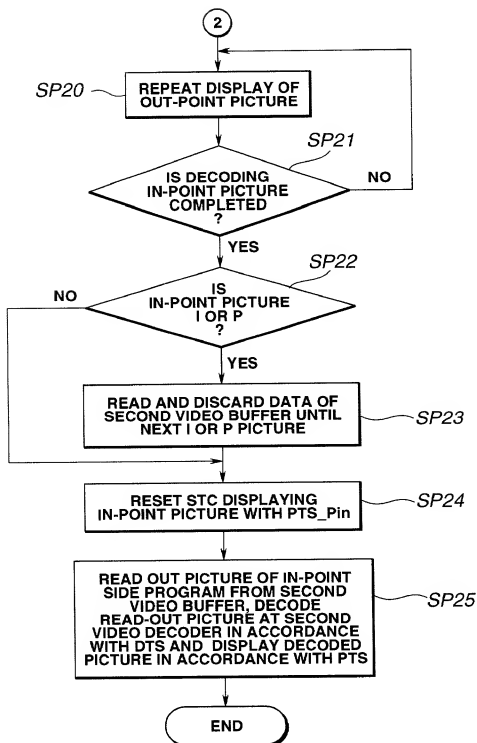


FIG.13



**FIG.14**

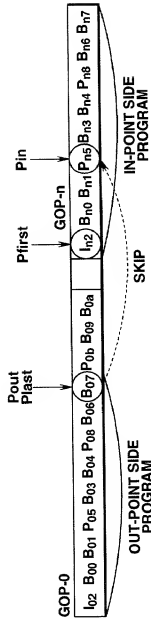


FIG.15A

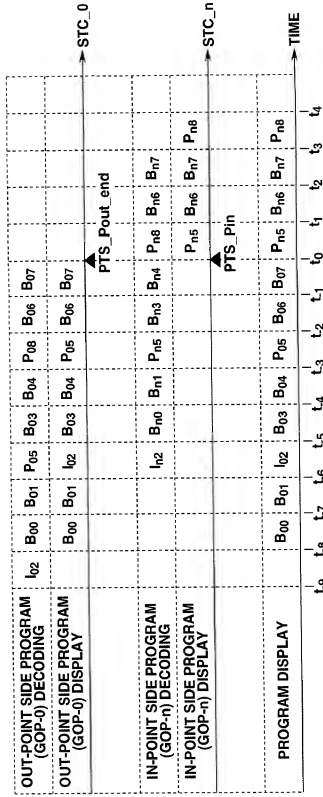


FIG.15B

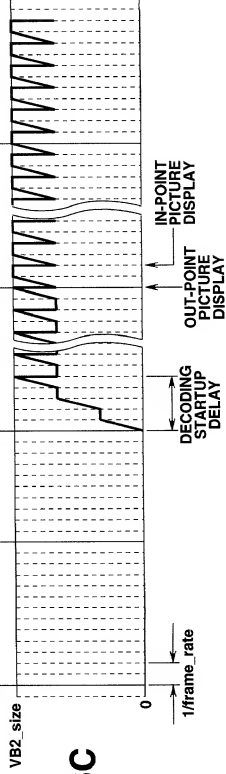
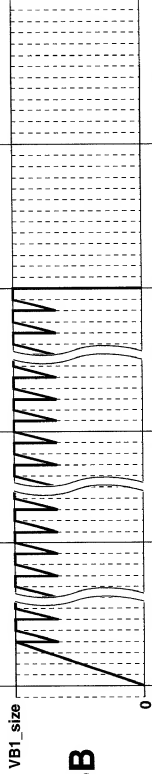
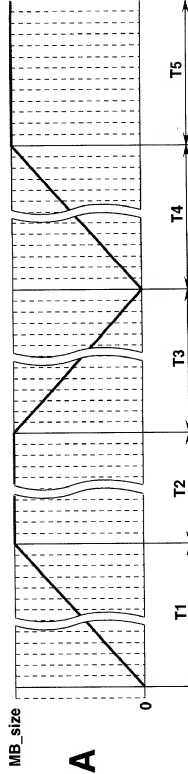
FIG.15C

FIG.15D

FIG.15E

FIG.15F

SWITCHING TIME FROM  
STC\_0 TO STC\_n  
(RESET STC AT PTS\_Pin)



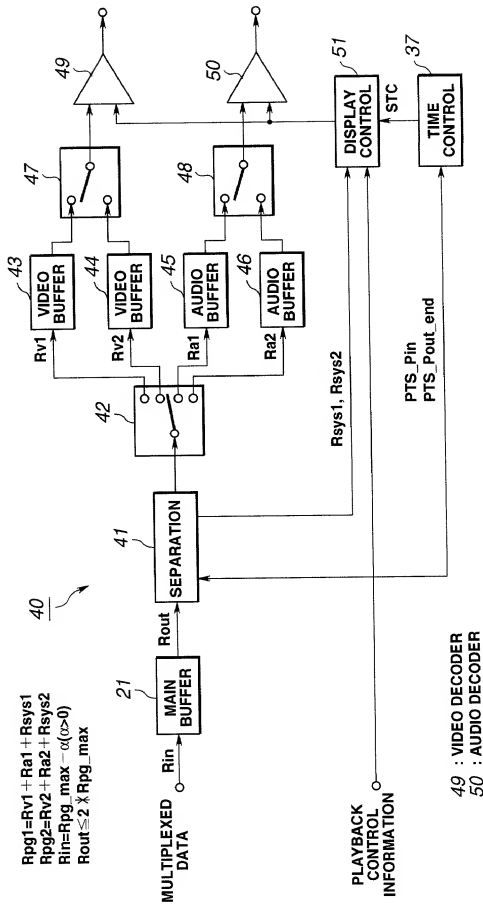


FIG.17



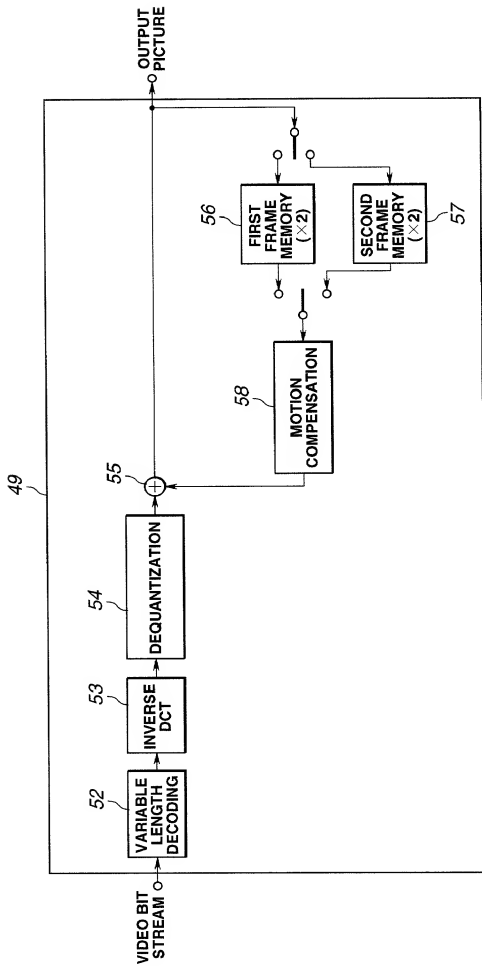


FIG.18

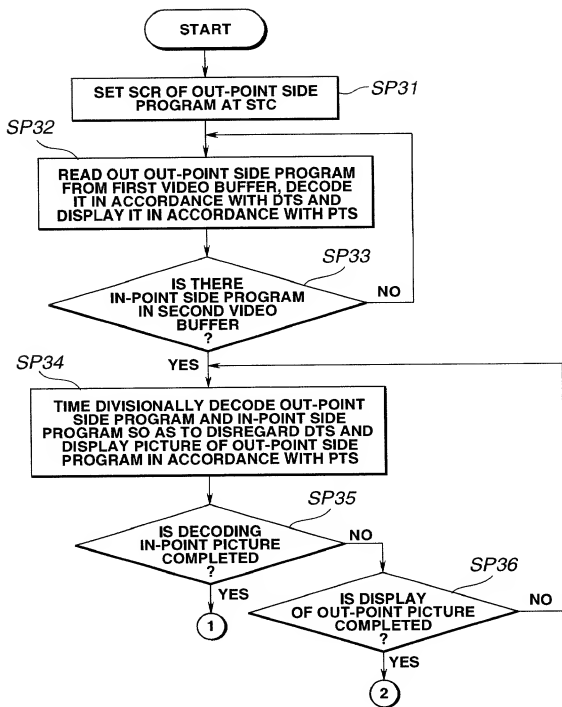


FIG.19

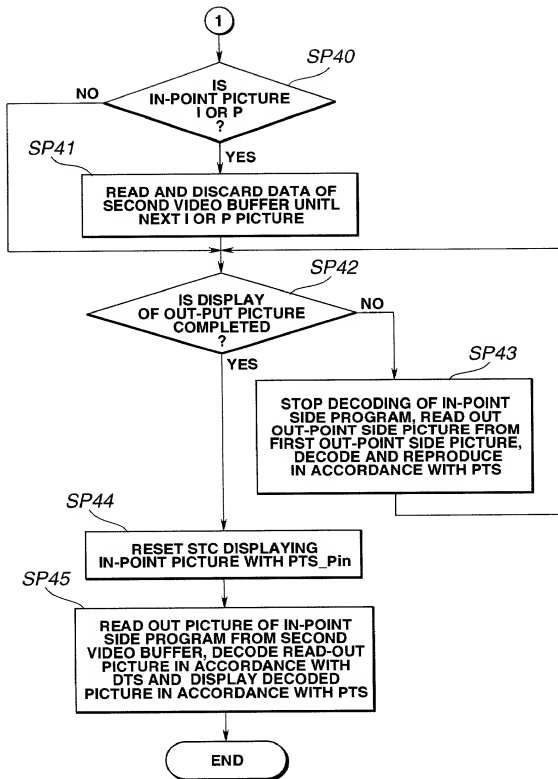


FIG.20

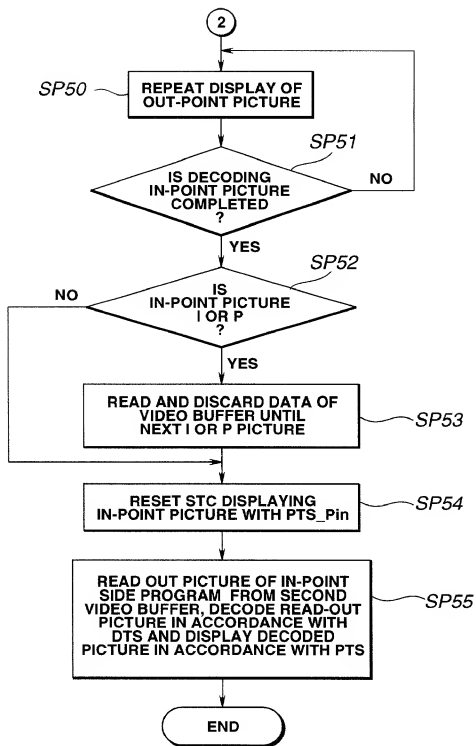


FIG.21

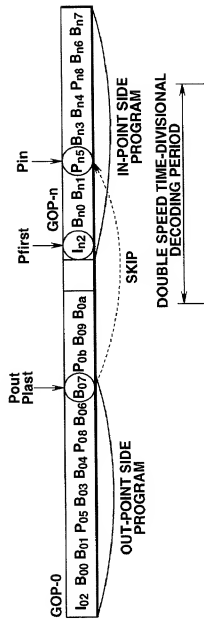


FIG.22A

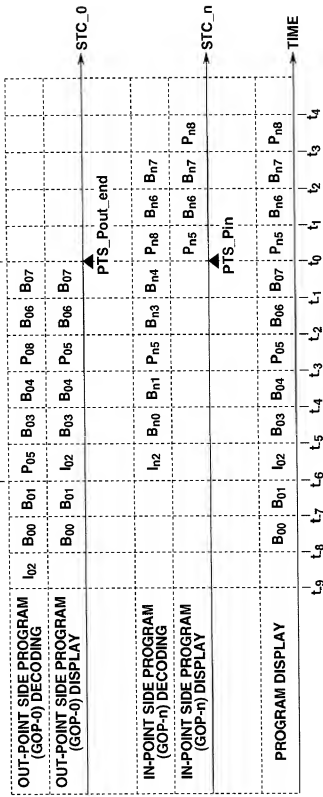


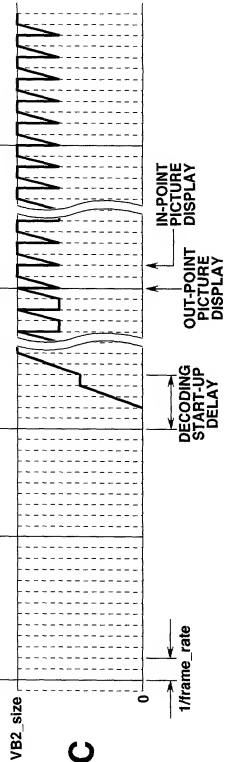
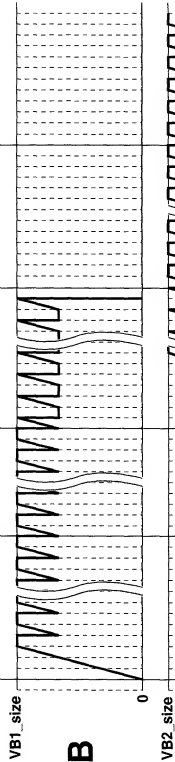
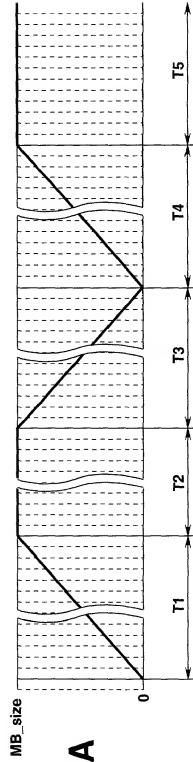
FIG.22B

FIG.22C

FIG.22D

FIG.22E

FIG.22F



IN-POINT  
PICTURE  
DISPLAY

OUT-POINT  
PICTURE  
DISPLAY

DECODING  
START-UP  
DELAY

1/frame\_rate

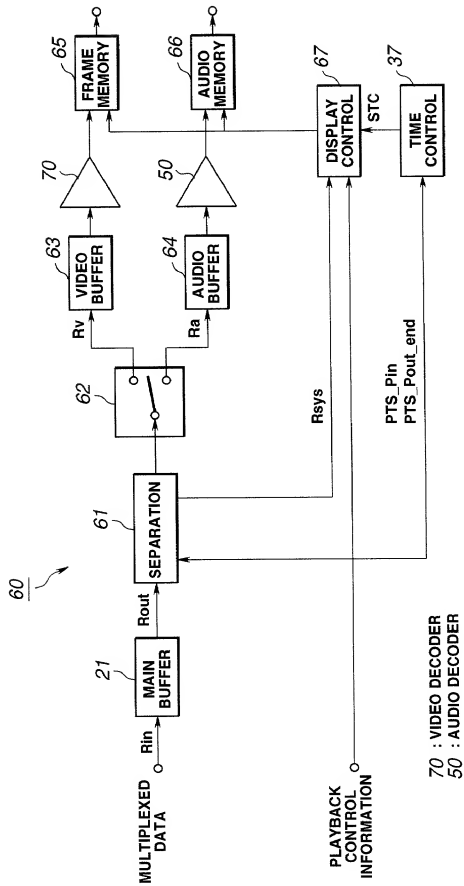
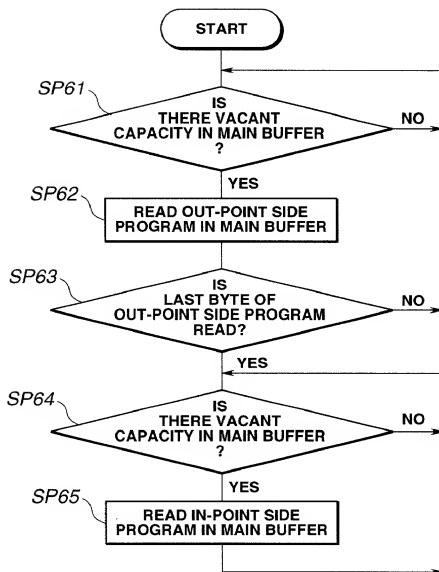


FIG.24



**FIG.25**



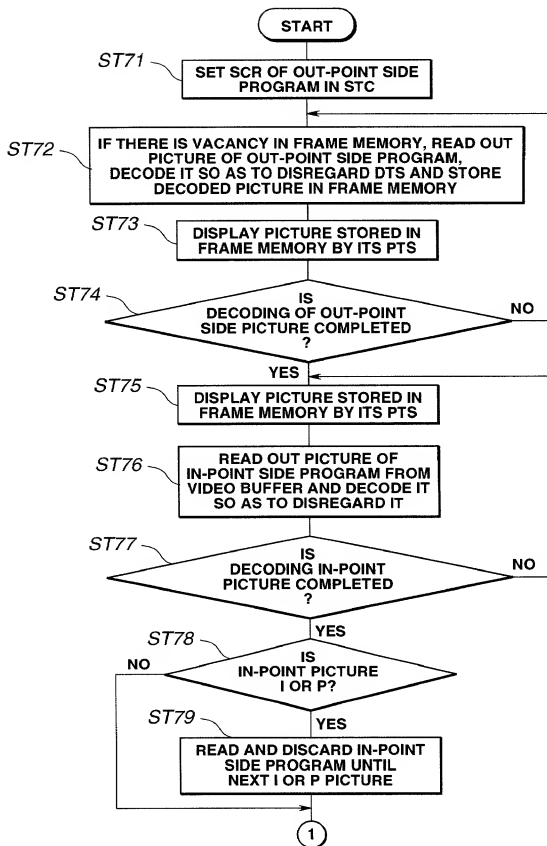


FIG.26

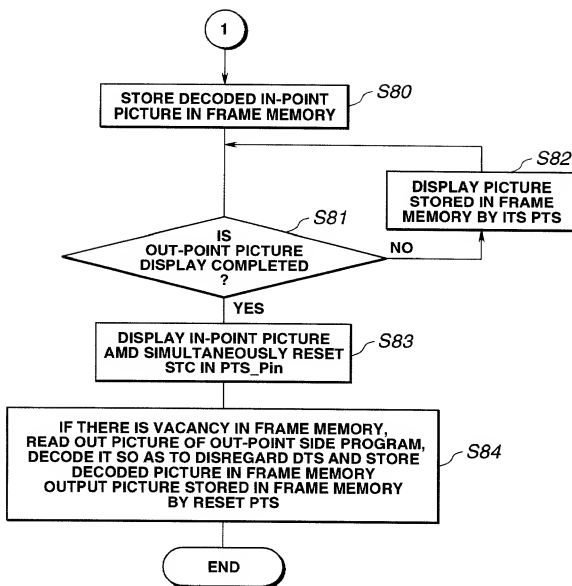


FIG.27

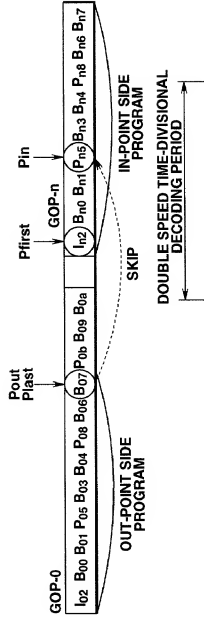


FIG.28A

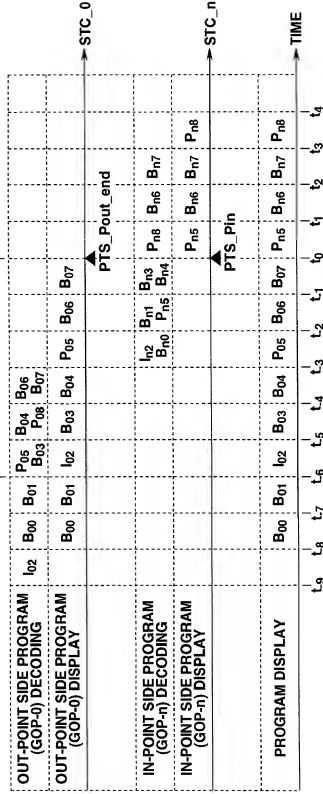


FIG.28B

FIG.28C

FIG.28D

FIG.28E

FIG.28F

